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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First-Named Inventor: LEIJTEN, Jeroen	Docket No.: NL 010073
Application No.: 10/059427 Conf.:	Art Unit: 2183
Date Filed: 01/29/2002	Examiner: Rizzuto, Kevin
Title: COMPUTER INSTRUCTION WITH INST	TRUCTION FETCH CONTROL BITS
Commissioner for Patents P.O. Box 1450 Arlington, VA 22313-1450	
TRANSMITTAL LETTER	
Sir:	
There are a total of 2 pages in this submiss	ion which includes this transmittal letter, as well
as the documents listed below:	
Response to Restriction Requireme	ent - 1 page
The Commissioner is hereby requested and authorized pursuant to 37 CFR §1.136(a)(3), to	
treat any concurrent or future reply in this application requiring a petition for extension of time for its	
timely submission, as incorporating a petition for extension of time for the appropriate length of	
time. Please charge any additional fees which may	
application, including extension of time fees, but e	excluding the issue fee unless explicitly requested
to do so, and credit any overpayment, to Deposit Account No. 14-1270.	
The state of the s	
Date: <u>(12/04</u> R	despectfully submitted,
В	Michael J. Ure, Reg. No. 33,089 (408) 474-907/ Philips Electronics North America Corp. 1109 McKay Drive, M/S 41-SJ San Jose, CA 95131
	113-1450," or fagsishile transmitted to the

NOV 0 2 2004 US 000089

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Atty. Docket

LEUTEN, JEROEN A.J.

NL 010073

Serial: 10/059,427

Group Art Unit: 2183

Filed: 01/29/2002

Examiner: RIZZUTO, KEVIN P

COMPUTER INSTRUCTION WITH INSTRUCTION FETCH CONTROL BITS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

Responsive to the Restriction Requirement of 08/19/2004, Applicant hereby elects the claims of Group 1 (claims 1-12).

Respectfully submitted,

Michael J. Ure, Reg. 33,089

Dated: November 1, 2004